

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Previously amended) A method of optimizing at least two target machines, comprising the steps of:

abstracting a rule of instruction scheduling for each of said at least two target machines;

generating a hypothetical machine based on said rule of instruction scheduling for each of said at least two target machines; and

targeting said hypothetical machine.

2. (Previously amended) The method of claim 1 wherein a different rule of instruction scheduling for said hypothetical machine is a restrictive set of said rule of instruction scheduling for each of said at least two target machines.

3. (Previously amended) The method of claim 1 further including the steps of:
detecting a conflict between said rule of instruction scheduling for each of said at least two target machines; and
resolving said conflict.

4. (Previously amended) The method of claim 3 wherein said step of resolving said conflict includes a step of selecting a less damaging option for said detected conflict.

5. (Previously amended) The method of claim 3 wherein said detected conflict corresponds to an inherent conflict between said rule of instruction scheduling for each of said at least two target machines.

6. (Original) The method of claim 1 further including the steps of:
modeling each of said at least two target machines; and
retrieving scheduling information corresponding to each of said at least two target machines.

7. (Original) The method of claim 1 wherein said at least two target machines include an UltraSPARC-II configured to operate at a speed of 360 MHz and an UltraSPARC-III configured to operate at a speed of 600 MHz.

8. (Previously amended) A method of optimizing at least two target machines, comprising the steps of:

retrieving scheduling information corresponding to each of said at least two target machines;

abstracting a rule of instruction scheduling for each of said at least two target machines;

generating a hypothetical machine based on said rule of instruction scheduling for each of said at least two target machines; and

targeting said hypothetical machine.

9. (Previously amended) The method of claim 8 further including the steps of:
detecting a conflict between said rule of instruction scheduling for each of said at
least two target machines; and
resolving said conflict.

10. (Previously amended) The method of claim 9 wherein a different rule of
instruction scheduling for said hypothetical machine is a restrictive set of said rule of
instruction scheduling for each of said at least two target machines.

11. (Previously amended) The method of claim 9 wherein said step of resolving
said conflict includes a step of selecting a less damaging option for said detected conflict.

12. (Previously amended) The method of claim 9 wherein said detected conflict
corresponds to an inherent conflict between said rule of instruction for each of said at least
two target machines.

13. (Previously amended) An apparatus for optimizing at least two target
machines, comprising:

means for abstracting a rule of instruction scheduling for each of said at least two
target machines;

means for generating a hypothetical machine based on said rule of instruction
scheduling for each of said at least two target machines; and

means for targeting said hypothetical machine.

14. (Previously amended) The apparatus of claim 13 wherein a different rule of instruction scheduling for said hypothetical machine is a restrictive set of said rule of instruction scheduling for each of said at least two target machines.

15. (Previously amended) The apparatus of claim 13 further including:
means for detecting a conflict between said rule of instruction scheduling for each of said at least two target machines; and
means for resolving said conflict.

16. (Previously amended) The apparatus of claim 15 wherein said resolving means includes means for selecting a less damaging option for said detected conflict.

17. (Previously amended) The apparatus of claim 15 wherein said detected conflict corresponds to an inherent conflict between said rule of instruction scheduling for each of said at least two target machines.

18. (Original) The apparatus of claim 13 further including:
means for modeling each of said at least two target machines; and
means for retrieving scheduling information corresponding to each of said at least two target machines.

19. (Previously amended) An apparatus for optimizing at least two target machines, comprising:
means for retrieving scheduling information corresponding to each of said at least two target machines;

means for abstracting a rule of instruction scheduling for each of said at least two target machines;

means for generating a hypothetical machine based on said rule of instruction scheduling for each of said at least two target machines; and

means for targeting said hypothetical machine.

20. (Previously amended) An apparatus for optimizing a plurality of target machines, comprising:

means for modeling a plurality of target machines;

means for retrieving scheduling information corresponding to each of said plurality of target machines;

means for abstracting a rule of instruction scheduling for each of said plurality of target machines;

means for generating a hypothetical machine based on said rule of instruction scheduling for each of said plurality of target machines;

means for target said hypothetical machine;

means for detecting a conflict between said rule of instruction scheduling for each of said plurality of target machines; and

means for resolving said conflict.